



Application Notes for Tantalum Solid Electrolytic Capacitor

1. Operating Voltage

Tantalum Solid Electrolytic Capacitor shall be operated at the rated voltage or lower.

Rated voltage: The "rated voltage" refers to the maximum DC voltage that is allowed to be continuously applied between the capacitor terminals at the rated temperature.

Surge voltage: The "surge voltage" refers to the voltage that is allowed to be instantaneously applied to the capacitor at the rated temperature or the maximum working temperature. The capacitor shall withstand the voltage when a 30-second cycle of application of the voltage through a 1000 Ω series resistance is repeated 1000 times in 6-minute periods.

When designing the circuit, the equipment's required reliability must be considered and appropriate voltage derating must be performed.

2. Application that contain AC Voltage

Special attention to the following 3 items.

- (1) The sum of the DC bias voltage and the positive peak value of the AC voltage should not exceed the rated voltage.
- (2) Reverse voltage should not exceed the allowable values of the negative peak AC voltage.
- (3) Ripple current should not exceed the allowable values.

3. Reverse Voltage

Tantalum solid electrolytic capacitor is polarity. Please do not impress reverse voltage. As well, please confirm the potential of the tester beforehand when both ends of the capacitor are checked with the tester etc.

4. Permissible Ripple Current

The permissible ripple current and voltage at about 100 kHz or higher can be determined by the following formula from the permissible power loss (Pmax value) shown in Table 1 and the specified ESR value. However, when the expected operating temperature is higher than room temperature, determine the permissible values multiplying the Pmax value by the specified multiplier (Table 2). For the permissible values at different frequencies, consult our Sales Department.

$$P=I^2 \times ESR \text{ or } P=\frac{E^2 \times ESR}{Z^2}$$

$$\text{Permissible ripple current } I_{max} = \sqrt{\frac{P_{max}}{ESR}} \text{ (Arms)}$$

$$\begin{aligned} \text{Permissible ripple voltage } E_{max} &= \sqrt{\frac{P_{max}}{ESR}} \times Z \\ &= I_{max} \times Z \text{ (Vrms)} \end{aligned}$$

I_{max} : Permissible ripple current at regulated frequency (Arms : RMS value)

E_{max} : Permissible ripple voltage at regulated frequency (Vrms : RMS value)

P_{max} : Permissible power loss (W)

ESR : Specified ESR value at regulated frequency (Ω)

Z : Impedance at regulated frequency (Ω)

Table 1 Permissible power loss

Case size	Pmax (W)
06U	0.030
09M	0.050
09M,10M (Specification Number 500)	0.057
09S,10S,12S,13S	0.065
09A,10A,12A,13A	0.078

Table 2 Pmax multiplier at each operating temperature

Operating temperature (°C)	Multiplier
25	1.0
55	0.9
85	0.8
125	0.4

Note: Above values are measured at 0.8t glass epoxy board mounting in free air and may be changed depending on the kind of board, packing density, and air convection condition. Please consult us if calculated power loss value is different from above list of P max value.

5. Application on low-impedance circuit

The failure rate of low impedance circuit at 0.1Ω/V is about five times greater than that of a 1Ω/V circuit. To curtail this higher failure rate, tantalum capacitors used in low impedance circuits, such as filters for power supplies, particularly switching power supplies, or for noise by-passing, require that operating voltage be derated to less than half of the rated voltage. Actually, less than 1/3 of the rated voltage is recommended.

6. Non Polar Application(BACK TO BACK)

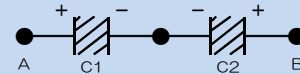
Tantalum capacitors can be used as a non-polar unit if two capacitors are connected "BACK-TO-BACK" when reserve voltage is applied at a more than permissible value, or in a purely AC circuit. The two capacitors should both be of the same rated voltage and capacitance tolerance, and they should both be twice the required capacitance value.

Ripple Voltage: Permissible Ripple Voltage shall not exceed the value allowed for either C1 or C2 (This will be the same, as the capacitors should be identical.)

Capacitance: $(C1 \times C2) / (C1 + C2)$

Leakage Current: If terminal A is (+), the Leakage Current will be equal to C1's Leakage Current.

If terminal B is (+), the Leakage Current will be equal to C2's Leakage Current.



7. Soldering

7.1. Preheating

To obtain optimal reliability and solderability conditions, capacitors should be pre-heated at 130 to 200 °C for approximately 60 to 120 seconds.

7.2. Soldering

The body of the capacitor shall not exceed 260 °C during soldering.

(1) Reflow Soldering

Reflow soldering is a process in which the capacitors are mounted on a printed board with solder paste. There are two methods of Reflow Soldering: Direct and Atmospheric Heat.

· Direct Heat (Hot plate)

During the Direct Heat method, the capacitor has been positioned on a printed board, which is then placed upon a hot plate.

The capacitor maintains a lower temperature than the substrate, which in turn stays at a lower temperature than the hot plate.

· Atmospheric Heat

a) VPS (Vapor Phase Soldering)

During VPS, the substrate is heated by an inert liquid with a high boiling point. The temperature of the capacitor's body and the temperature of the substrate are about the same as the atmosphere. This temperature should be below 240°C.

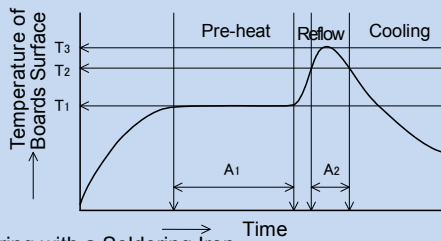
b) Near and Far IR Ray

Due to the heat absorption of the capacitor's body, the internal temperature of the capacitors may be 20 ~ 30°C higher than the setting temperature and may exceed 260°C.

Temperature control is crucial in maintaining a temperature of 260 °C or lower.

c) Convection Oven

An infrared ray is the main source of heat in this process. The temperature of the substrate and the capacitors can be maintained at a similar level by the circulation of heated air, or an inert gas.



Temperature	Time
T1=130°C~200°C	A1= 60~120sec.
T2=220°C~230°C	A2<60sec.
T3~260°C	10 sec. or less than 10

Number of times : 2 times max..

(2) Soldering with a Soldering Iron

Soldering with a soldering iron cannot be recommended due to the lack of consistency in maintaining temperatures and process times. If this method should be necessary, the iron should never touch the capacitor's terminals, and the temperature of the soldering iron should never exceed 350°C. The application of the iron should not exceed 5 seconds.

(3) Please consult us for other methods.

8. Cleaning

Cleaning by organic solvent may damage capacitor's appearance and performance. However, our capacitors are not effected even when soaked at 20 ~ 30°C 2-propanol for 5 minutes. When introducing new cleaning methods or changing the cleaning term, please consult us.

9. Protective Resin Coating

After components are assembled to substrate, a protective resin coating is sometimes applied. As this resin coating cures, it gives mechanical and thermal stress to Tantalum capacitors. This stress can cause damage to the capacitors, which affects their reliability. Before using a resin coating, proper research must be done in regards to the material and process to insure that excessive stress will not be applied to capacitors and other components.

10. Vibration

Approximately 300 G shall be applied to a capacitor, when dropped from 1 meter to a concrete floor.

Although capacitors are made to withstand this drop test, stress from shock due to falling or striking does cause damage to the capacitors and increases failure rates. Do not subject capacitors to this type of mechanical stress.

11. Ultrasonic cleaning

Matsuo does not recommend Ultrasonic cleaning. This may cause damage to the capacitors, and may even cause broken terminals. If the Ultrasonic cleaning process will be used, please note the following:

(1)The solvent should not be boiled. (Lower the ultrasonic wave output or use solvent with The high boiling point.)

(2)The recommended wattage is less than 0.5 watts per cm².

(3)The cleaning time should be kept to a minimum. Also, samples must be swang in the solvent. Please consult us.

12. Additional Notes

· When more than one capacitor is connected in series, a resistor that can distribute the voltage equally to the capacitors shall be connected in parallel.

· The capacitor cases shall not be cut even if the mounting space is insufficient.

· During a customers aging process, voltage should remain under the rated voltage at all times.

· Capacitors should never be touched or manipulated while operating.

· Capacitors are not meant to be dismantled.

· When testing capacitors, please examine the power source before conducting test to insure the tester's polarity and applied voltage.

· In the event of a capacitor burning, smoking, or emitting an offensive smell during operation, please turn the circuit "off" and keep hands and face away from the burning capacitor.

· If a capacitor be electrical shorted, it becomes hot, and the capacitor element may ignite.

In this case, the printed board may be burnt out.

· Capacitors should be stored at room temperature under low humidity. Capacitors should never be stored under direct sunlight, and should be stored in an environment containing dust.

· If the capacitors will be operated in a humid environment, they should be sealed with a compound under proper conditions.

· Capacitors should not be stored or operated in environments containing acids, alkalis or active gasses.

· When capacitors are disposed of as "scrap" or waste, they should be treated as Industria Waste since they contain various metals and polymers.

· Capacitors submitted as samples should not be used for production purposes.

These application notes are prepared based on "Guideline of notabilia for fixed tantalum electrolytic capacitors with solid electrolyte for use in electronic equipment" (EIAJ RCR-2368) issued by Japan Electronics and Information Technology Industries Association (JEITA). For the details of the instructions (explanation, reasons and concrete examples), please refer to this guideline, or consult our Sales Department.



MATSUO ELECTRIC CO., LTD.

Please feel free to ask our Sales Department for more information on Tantalum Solid Electrolytic Capacitor.

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